

**PATENT NUMBER**


# U.S. UTILITY Patent Application

O.I.P.E. SCANNED <u>09/3</u> Q.A. <u>SW</u>	PATENT DATE
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APPLICATION NO. 09/785452	CONT/PRIOR F	CLASS 257	SUBCLASS 673	ART UNIT <del>2811</del> 2815	EXAMINER <del>EDDIE LEE</del>
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### 37111

Semiconductor integrated circuit device and method of manufacturing the same

**PTO-2040**  
**12/99**

ISSUING CLASSIFICATION												
ORIGINAL					CROSS REFERENCE(S)							
CLASS		SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>  	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b> Amount Due Date Paid	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	

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